



S/N 10/774,956

PATENTIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Donald S. Gardner	Examiner:	Tuan H. Nguyen
Serial No.:	10/774,956	Group Art Unit:	2813
Filed:	February 9, 2004	Docket:	884.140US3
Title:	INDUCTORS FOR INTEGRATED CIRCUITS (AS AMENDED)		
Assignee:	Intel Corporation	Customer No.	21186

COMMUNICATION CONCERNING RELATED APPLICATION(S)

MS Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Applicant would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

<u>Serial/Patent No.</u>	<u>Filing Date/Issue Date</u>	<u>Attorney Docket</u>	<u>Title</u>
09/444608	November 23, 1999	884.140US1	INDUCTOR FOR INTEGRATED
6452247	September 17, 2002		CIRCUIT
10/238539	September 10, 2002	884.140US2	METHODS FOR FABRICATING
6727154	April 27, 2004		INDUCTOR FOR INTEGRATED
			CIRCUIT OR INTEGRATED CIRCUIT
			PACKAGE

Continuations and divisionals may be later filed on the cases listed above, or cited to the Examiner in any previous Communication Concerning Related Applications. Applicants request that the Examiner review all continuations and divisionals of the above-listed or previously-cited patent applications before allowing the claims of the present patent application.

Respectfully submitted,  
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Date Feb. 3, 2006 By Ann M. McCrackin  
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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 16 day of February, 2006.

Chris Hammond  
Name

Chris Hammond  
Signature